

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Application Number	10/661,879
Filing Date	9/11/2003
First Named Inventor:	ELDER, Joseph S.
Examiner name: <del>Unknown</del> <i>E. Vuong</i>	GROUP: <del>Unknown</del> 2618
Attorney Docket Number	MIC-05632-5D

Sheet 1 of 3

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cited No. <sup>1</sup>	U.S. Patent Document Number	Kind Code <sup>2</sup> (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear
QV	A01	4,336,613		Hewes	6/22/1982	
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	A03	5,012,490		Myer	4/30/1991	
	A04	5,020,147		Okanobu	5/28/1991	
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	A06	5,134,406		Orr	7/28/1992	
	A07	5,263,192		Mittel et al.	11/1993	
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	A15	5,710,997		Okanobu	1/20/1998	
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	A18	5,933,769		Kaneko	8/3/1999	
	A19	5,963,857		Greywall	10/1999	
	A20	6,167,246		Elder et al.	12/26/2000	
	A21	6,278,866	B1	Elder et al.	8/21/2001	
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QV	A23	6,377,788	B1	Elder et al.	4/23/2002	

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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.



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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cited No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
QV	A24	WO	98/49779			11/5/98		
QV	A25	WO	99/59256			11/11/99		

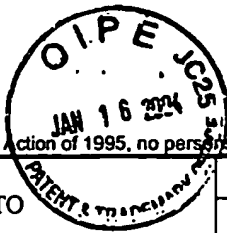
**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

QV	PCT Written Opinion for International Application No. PCT/US/99/09995, filed May 6, 1999.							
	Abidi, Asad A., "CMOS-only RF and Baseband Circuits for a Monolithic 900 MHz Wireless Transceiver*," Bipolar Circuits & Tech. Mtg., Minneapolis, October 1996, pp. 35-42.							
	Abidi, Asad A., "Radio-Frequency Integrated Circuits for Portable Communications," <i>Custom IC Conference</i> , San Diego, CA, May 1994, pp. 151-158.							
	Chang, P. J., Rofougaran, A., and Abidi, A. A., "A CMOS Channel-Select Filter for a Direct-Conversion Wireless Receiver," <i>Symp. On VLSI Circuits</i> , Honolulu, pp. 62-63, 1996.							
	Chang, J., Abidi, A. A., and Viswanathan, C.R., "Flicker Noise in CMOS Transistors from Subthreshold to Strong Inversion at Various Temperatures," <i>IEEE Trans. On Electron Devices</i> , vol. 41, no. 11, pp. 1965-1971, 1994.							
	Khorram, S. et al., "A CMOS Limiting Amplifier & Logarithmic Signal-Strength Indicator," <i>Integrated Circuits &amp; Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles</i> (19 pages).							
	Rofougaran, A., et al., "A 900 MHz CMOS Frequency-Hopped Spread Spectrum RF Transmitter IC," <i>Custom Integrated Circuits Conf.</i> , San Diego, CA, May 1996, pages 209-212.							
	Rofougaran, A. et al., "A 900 MHz CMOS Frequency-Hopped Spread Spectrum RF Transmitter IC," <i>Integrated Circuits &amp; Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles</i> (20 pages).							
	Rofougaran, A., Chang, J. Y.-C., Rofougaran, M., Khorram, S., and Abidi, A. A., "A 1 GHz CMOS RF Front-End IC with Wide Dynamic Range," presented at <i>European Solid-State Circuits Conf.</i> , Lille, France, pp. 250-253, 1995							
↓	Rofougaran, A., Chang, J. Y.-C., Rofougaran, M., Khorram, S., and Abidi, A. A., "A 1 GHz CMOS RF Front-End IC with Wide Dynamic Range," (23 pages).							
QV	Rofougaran, A., et al., "A 900 MHz CMOS LC-Oscillator with Quadrature Outputs," presented at the 1996 Int'l. Solid-State Circuits Conference, 10 pages.							

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QV	Rudell, Jacques C., University of California, Berkeley, "CMOS VLSI Frequency Translation for Multi-Standard Wireless Communication Transceivers," Qualifying Examination, April 8, 1996, pp. 1-24.
QV	Chang, Glenn et al., "An Agile, Low-Power CMOS Digitally Synthesized 0-13 MHz Sinewave Generator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles, (14 pages).

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